

Adjoint Logic: Multiple Modalities into a Computational Framework

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Joint work with Brigitte Pientka

Adjoint Logic as a Uniform Framework

next Modality

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next Modality
Binding-time Analysis
Model Checking

□ Modality

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Staged programming

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Information Flow

Reactive Programming

while keeping static/dynamic semantics!

Adjoint Logic

Uniform logic with two adjoint functors \uparrow (upshift)/ \downarrow (downshift)
connecting different sublogics:

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However,

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- ▶ No syntactic operational/reduction semantics
- ▶ No computational interpretation for \uparrow/\downarrow

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“ELEVATOR”

Practical and uniform foundation for proof/programming about modalities with

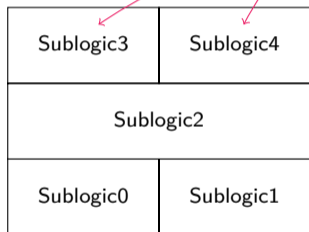
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“ELEVATOR”

Overview of ELEVATOR

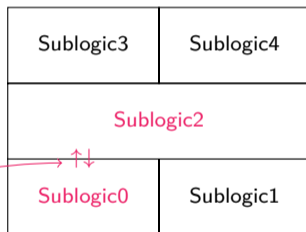
Sublogic3	Sublogic4
Sublogic2	
Sublogic0	Sublogic1

Overview of ELEVATOR



There are multiple sublogics
each of which resides in one "mode"

Overview of ELEVATOR



Adjoint modalities \uparrow and \downarrow allow interaction between sublogics

Mode Specification

Modes are members of a **preorder**

Each mode controls allowed **structural rules** and **types** in its sublogic.

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For example,

$u - \{\text{Wk}, \text{Co}\}, \{\uparrow\}$
$l - \{\}, \{\downarrow, \multimap, \text{Nat}\}$

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(where $u \geq l$)

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Dual intuitionistic linear logic (DILL) [Barber and Plotkin, 1996]

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Linear/non-linear logic (LNL) [Benton, 1995]

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Higher, More global, More long-lasting



Lower, More local, More temporary

(where $u \geq l$)

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Weakening

(where $u \geq l$)

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For example,

$u - \{\text{Wk}, \text{Co}\}, \{\uparrow\}$
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Contraction

$u - \{\text{Wk}, \text{Co}\}, \{\uparrow, \rightarrow, \text{Nat}\}$
$l - \{\}, \{\downarrow, \multimap, \text{Nat}\}$

(where $u \geq l$)

Mode Specification

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$l - \{\}, \{\downarrow, \multimap, \text{Nat}\}$

(where $u \geq l$)

Mode Dependency Principle

The behaviour of a sublogic of a **higher mode**
cannot depend on
the behaviour of a sublogic of a **lower mode**.

Adjoint Modalities — Computational Interpretation

Adjoint modalities \uparrow and \downarrow connect two comparable modes:

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- ▶ $\uparrow_l^h S$ — A thunk at a mode h of a closed deferred expression of type S at a lower mode l

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Adjoint modalities \uparrow and \downarrow connect two comparable modes:

- ▶ $\uparrow_l^h S$ — A thunk at a mode h of a closed deferred expression of type S at a lower mode l
- ▶ $\downarrow_l^h S$ — A pointer at a mode l to a stored value of type S at a higher mode h

Computational Interpretation of $\uparrow_i^h S$

Computational Interpretation of $\uparrow_l^h S$

► $\uparrow_l^h S$ — A thunk at a mode h of a closed deferred expression of type S at a lower mode l

Computational Interpretation of $\uparrow_l^h S$

- ▶ $\text{thunk}_l^h(L) : \uparrow_l^h S$ — A thunk at a mode h of a closed deferred expression L of type S at a lower mode l

Computational Interpretation of $\uparrow_l^h S$

- ▶ $\text{thunk}_l^h (L) : \uparrow_l^h S$ — A thunk at a mode h of a closed deferred expression L of type S at a lower mode l
- ▶ $\text{force}_l^h (L) : S$ — compose/execute the expression in the thunk $L : \uparrow_l^h S$

Computational Interpretation of $\downarrow_i^h S$

Computational Interpretation of $\downarrow_l^h S$

- ▶ $\downarrow_l^h S$ — A pointer at a mode l to a stored value of type S at a higher mode h

Computational Interpretation of $\downarrow_l^h S$

- ▶ $\text{store}_l^h (L) : \downarrow_l^h S$ — A pointer at a mode l to a stored value of L of type S at a higher mode h

Computational Interpretation of $\downarrow_l^h S$

- ▶ $\text{store}_l^h (L) : \downarrow_l^h S$ — A pointer at a mode l to a stored value of L of type S at a higher mode h
- ▶ $\text{load}_l^h (x) = L$ in M — load the value of type S referred by the pointer $L : \downarrow_l^h S$ into x and continue with M

λ^{\square} — A foundation for staged programming

λ^{\square} [Davies and Pfenning, 2001]

- ▶ $\square A$ describes a code fragment of type A

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$c - \{Wk, Co\}, \{\uparrow\}$
$p - \{Wk, Co\}, \{\downarrow, \rightarrow, Nat\}$

Staging the Power Function

$c - \{\text{Wk}, \text{Co}\}, \{\uparrow\}$
$p - \{\text{Wk}, \text{Co}\}, \{\downarrow, \rightarrow, \text{Nat}\}$

```
1 pow :P Nat → ↓pc ↑pc (Nat → Nat)
2 pow 0      =
3   storepc (thinkpc (fun x → 1))
4 pow (suc n) =
5   loadpc P = pow n in
6   storepc (thinkpc (fun x → x * ((forcepc P) x)))
```

Staging the Power Function

$c - \{Wk, Co\}, \{\uparrow\}$

$p - \{Wk, Co\}, \{\downarrow, \rightarrow, Nat\}$
--

Encoding of \square

```
1 pow :P Nat →  $\downarrow_p^c \uparrow_p^c$  (Nat → Nat)
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local memory	global memory	persistent storage

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local memory	global memory	persistent storage

Staging the Power Function

$c - \{Wk, Co\}, \{\uparrow\}$

$p - \{Wk, Co\}, \{\downarrow, \rightarrow, Nat\}$

Construct a thunk of type $\uparrow_p^c(Nat \rightarrow Nat)$
for $\text{fun } x \rightarrow 1$ (for x^0)

```
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local
memory

global
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local memory	global memory	persistent storage

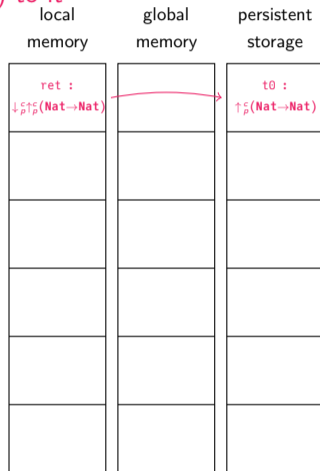
Staging the Power Function

c - {Wk, Co}, {↑}

p - {Wk, Co}, {↓, →, Nat}

Store it into t0 and return a pointer (ret) to it

```
1 pow :P Nat → ↓pc ↑pc (Nat → Nat)
2 pow 0 =
3   storepc (thunkpc (fun x → 1))
4 pow (suc n) =
5   loadpc P = pow n in
6   storepc (thunkpc (fun x → x * ((forcepc P) x)))
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Staging the Power Function

c - $\{\text{Wk}, \text{Co}\}, \{\uparrow\}$

p - $\{\text{Wk}, \text{Co}\}, \{\downarrow, \rightarrow, \text{Nat}\}$

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1 pow :P Nat →  $\downarrow_p^c \uparrow_p^c$  (Nat → Nat)
2 pow 0      =
3   storepc (thinkpc (fun x → 1))
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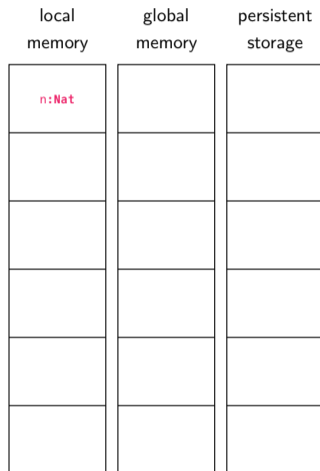
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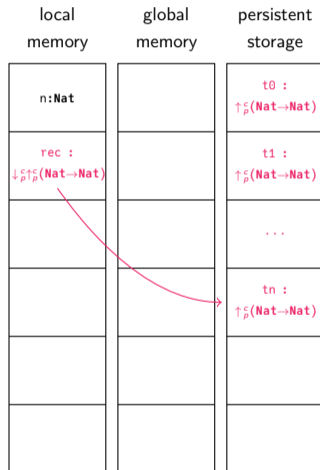
Staging the Power Function

c - $\{Wk, Co\}, \{\uparrow\}$

P - $\{Wk, Co\}, \{\downarrow, \rightarrow, Nat\}$

```
1 pow :P Nat → ↓Pc ↑Pc (Nat → Nat)
2 pow 0 =
3   storePc (thunkPc (fun x → 1))
4 pow (suc n) =
5   loadPc P = pow n in
6   storePc (thunkPc (fun x → x * ((forcePc P) x)))
```

Recursively build a thunk for x^n
and get a pointer (rec) to it



Staging the Power Function

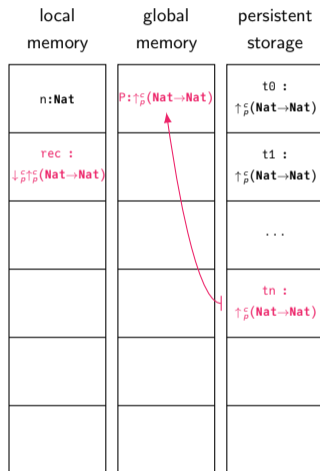
$c - \{Wk, Co\}, \{\uparrow\}$

$p - \{Wk, Co\}, \{\downarrow, \rightarrow, Nat\}$

Load the thunk referred by the pointer

```

1 pow :P Nat → ↓pc ↑pc (Nat → Nat)
2 pow 0 =
3   storepc (thunkpc (fun x → 1))
4 pow (suc n) =
5   loadpc P = pow n in
6   storepc (thunkpc (fun x → x * ((forcepc P) x)))
    
```



Staging the Power Function

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Splice P into a bigger thunk

```

1 pow :P Nat → ↓pc ↑pc(Nat → Nat)
2 pow 0      =
3   storepc (thunkpc (fun x → 1))
4 pow (suc n) =
5   loadpc P = pow n in
6   storepc (thunkpc (fun x → x * ((forcepc P) x)))
  
```

local memory	global memory	persistent storage
n:Nat	$P : \uparrow_p^c(\text{Nat} \rightarrow \text{Nat})$	t0 : $\uparrow_p^c(\text{Nat} \rightarrow \text{Nat})$
rec : $\downarrow_p^c \uparrow_p^c(\text{Nat} \rightarrow \text{Nat})$		t1 : $\uparrow_p^c(\text{Nat} \rightarrow \text{Nat})$
		...
		tn : $\uparrow_p^c(\text{Nat} \rightarrow \text{Nat})$

Staging the Power Function

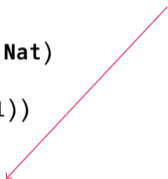
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```

Construct a thunk for x^{n+1}



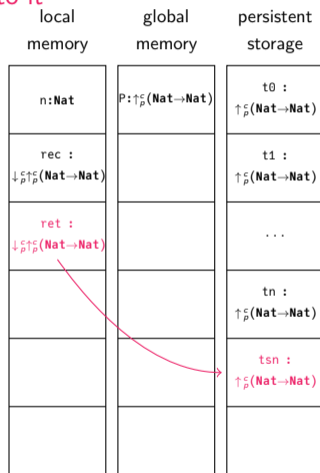
local memory	global memory	persistent storage
$n : \text{Nat}$	$P : \uparrow_p^c(\text{Nat} \rightarrow \text{Nat})$	$t_0 : \uparrow_p^c(\text{Nat} \rightarrow \text{Nat})$
$\text{rec} : \downarrow_p^c \uparrow_p^c(\text{Nat} \rightarrow \text{Nat})$		$t_1 : \uparrow_p^c(\text{Nat} \rightarrow \text{Nat})$
		...
		$t_n : \uparrow_p^c(\text{Nat} \rightarrow \text{Nat})$

Staging the Power Function

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$p - \{Wk, Co\}, \{\downarrow, \rightarrow, Nat\}$

Store it into tsn and get a pointer (ret) to it

```
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2 pow 0 =
3   storepc (thunkpc (fun x → 1))
4 pow (suc n) =
5   loadpc P = pow n in
6   storepc (thunkpc (fun x → x * ((forcepc P) x)))
```



Linear Calculus with !

$u - \{\text{Wk}, \text{Co}\}, \{\uparrow\}$
$! - \{\}, \{\downarrow, \multimap, \text{Nat}\}$

Working with a Protocol

$u - \{Wk, Co\}, \{\uparrow\}$
$l - \{\}, \{\downarrow, \rightarrow, Nat\}$

- ▶ Linear types can encode session types

Working with a Protocol

$u - \{Wk, Co\}, \{\uparrow\}$
$l - \{\}, \{\downarrow, \rightarrow, Nat\}$

- ▶ Linear types can encode session types
- ▶ Unrestricted types can encode functional program

Linear Calculus with \square and $!$ – Protocol with Remote Execution

$u - \{\text{Wk}, \text{Co}\}, \{\uparrow\}$	$c - \{\}, \{\uparrow\}$
$l - \{\}, \{\downarrow, \multimap, \text{Nat}\}$	

Lambda Calculus with \bigcirc — Dead Code Analysis

$p - \{\text{Wk, Co}\}, \{\uparrow, \rightarrow, \text{Nat}\}$
$s - \{\text{Wk, Co}\}, \{\downarrow\}$

$$\bigcirc A = \uparrow_s^p \downarrow_s^p A$$

Lambda Calculus with next — Binding Time Analysis

$t_0 - \{\text{Wk, Co}\}, \{\rightarrow, \text{Nat}\}$
$t_1 - \{\text{Wk, Co}\}, \{\downarrow, \rightarrow, \text{Nat}\}$
...

$$\text{next } A = \downarrow_{t_{n+1}}^{t_n} A$$

$$\Gamma \vdash^m L : S$$

Current mode of type checking

$$\Gamma \vdash^m L : S$$

Typing Judgement

$x_1:^{n_1} T_1, x_2:^{n_2} T_2, \dots$ where $n_i \geq m$

$\Gamma \vdash^m L : S$

Typing Rules for Adjoint Modalities

$$\frac{\Gamma \vdash' L : S}{\Gamma \vdash^m \text{thunk}_j^m (L) : \uparrow_j^m S} \quad \frac{\Gamma|^h \vdash^h L : \uparrow_m^h S}{\Gamma \vdash^m \text{force}_m^h (L) : S}$$

$$\frac{\Gamma|^h \vdash^h L : S}{\Gamma \vdash^m \text{store}_m^h (L) : \downarrow_m^h S}$$

$$\frac{m \geq^{\mathcal{M}} n \quad \Gamma' \vdash^m L : \downarrow_m^h T \quad \Gamma'', x :^h T \vdash^n M : S}{\Gamma'; \Gamma'' \vdash^n \text{load}_m^h (x) = L \text{ in } M : S} \text{E}\downarrow$$

Typing Rules for Adjoint Modalities

$\Gamma|_h = \Gamma'$ cuts all entries in Γ not higher than h

$$\frac{\Gamma \vdash' L : S}{\Gamma \vdash^m \text{thunk}_j^m(L) : \uparrow_j^m S} \quad \frac{\Gamma|_h \vdash^h L : \uparrow_m^h S}{\Gamma \vdash^m \text{force}_m^h(L) : S}$$

$$\frac{\Gamma|_h \vdash^h L : S}{\Gamma \vdash^m \text{store}_m^h(L) : \downarrow_m^h S}$$

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Typing Rules for Adjoint Modalities

$\Gamma'; \Gamma'' = \Gamma$ splits Γ into two contexts

$$\frac{\Gamma \vdash^l L : S}{\Gamma \vdash^m \text{thunk}_l^m (L) : \uparrow_l^m S} \quad \frac{\Gamma|^h \vdash^h L : \uparrow_m^h S}{\Gamma \vdash^m \text{force}_m^h (L) : S}$$

$$\frac{\Gamma|^h \vdash^h L : S}{\Gamma \vdash^m \text{store}_m^h (L) : \downarrow_m^h S}$$

$$\frac{m \geq^{\mathcal{M}} n \quad \Gamma' \vdash^m L : \downarrow_m^h T \quad \Gamma'', x : ^h T \vdash^n M : S}{\Gamma'; \Gamma'' \vdash^n \text{load}_m^h (x) = L \text{ in } M : S} \text{E}\downarrow$$

$$L \longrightarrow L'$$

$$L \longrightarrow^{m \leq} L'$$

Operational Semantics

$L \longrightarrow L'$ — reduction of redex

$L \longrightarrow^{m \leq} L'$

Operational Semantics

$L \longrightarrow L'$ — reduction of redex

$L \longrightarrow^{m \leq} L'$ — reduction of redex at mode $\geq m$ in deferred expression

Theorem (Type Preservation)

For $\Gamma \vdash^n L : S$,

- 1 If $L \longrightarrow L'$, then $\Gamma \vdash^n L' : S$
- 2 For any mode m , if $L \longrightarrow^{m \leq} L'$, then $\Gamma \vdash^n L' : S$

Theorem (Type Preservation)

For $\Gamma \vdash^n L : S$,

- 1 If $L \longrightarrow L'$, then $\Gamma \vdash^n L' : S$
- 2 For any mode m , if $L \longrightarrow^{m \leq} L'$, then $\Gamma \vdash^n L' : S$

Theorem (Progress)

For $\Gamma \vdash^n L : S$,

- 1 Either L is a weak normal form or there exists L' such that $L \longrightarrow L'$
- 2 For any mode m , either L is a canonical form or there exists L' such that $L \longrightarrow^{m \leq} L'$

Theorem (Complete and Sound Translation)

There is an ELEVATOR instance that keeps the well-typedness of the λ^\square or DILL

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In other words, ELEVATOR can be used as a compilation target from those systems.

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Algorithmic Typing Judgement

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Algorithmic Typing Rules for Adjoint Modalities

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$$\frac{m \geq^{\mathcal{M}} n \quad \Gamma \vdash^m L : \downarrow_m^h T/\Gamma'_1 \quad \Gamma, x : ^h T \vdash^n M : S/\Gamma'_2}{\Gamma \vdash^n \text{load}_m^h(x) = L \text{ in } M : S/(\Gamma'_1; (\Gamma'_2 \setminus x : ^h T))} \text{E}\downarrow$$

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Equivalence between Two Typings

Theorem (Equivalence between two typings)

There exists Γ' such that $\Gamma \vdash^m L : S/\Gamma'$ and for any $x:^n T \in \Gamma$, $\Gamma' \setminus x:^n T \vdash^m L : S$ is true for some Γ'' if and only if $\Gamma \vdash^m L : S$.